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		Application Number	10/726,990
		Filing Date	December 3, 2003
		First Named Inventor	Di Ronza et al.
		Group Art Unit	
		Examiner Name	
Sheet	1	Attorney Docket No.	LLP103US

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS								
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>2</sup> (if known)				
CB	BA	DE	100 02 127	A1	Infineon Technologies	08/02/2001	Entire Document	✓
CB	BB	DE	101 10 469	A1	Infineon Technologies	09/26/2002	Entire Document	✓
CB	BC	DE	39 24 695	A1	Dresden Mikroelektronik	02/22/1990	Entire Document	✓
CB	BD	DE	101 09 335	C2	Infineon Technologies	09/05/2002	Entire Document	✓
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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CB	CA	"Increased Throughput for the Testing and Repair of RAM's with Redundancy", RAMSEY W. HADDAD, ANTON T. DAHBURA and ANUP B. SHARMA, EIII Transactions on Computers, Volume 40, No. 2, February 1991, Pgs. 154-166.	
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